

**IN THE SPECIFICATION**

*Kindly replace the paragraph beginning on page 7, line 24 and extending to page 8, with the following:*

Figure 5(b) illustrates an improved driving scheme according to an exemplary embodiment of this invention, in which a low voltage pulse is added to the driving sequence immediately after the complete driving pulse. If desired, it is allowed to have a time period with zero voltage between the driving pulse and the dc-balancing pulse ( $V_{dc-balancing}$ ) because the chosen low voltage of the dc-balancing pulse is only able to remove the remnant dc voltages on the pixel and is not able to change the optical performance, such that there is no visual effect.